In re Application of OSHINS et al.
Serial No. 09/558,469

Listing of the Claims:

1-12. (Canceled)

13. (Canceled)

14. (Canceled)

15. (Previously presented) A system for configuring a resource to communicate with a device, comprising:

a bus bridge to which the device is connected,

a first component configured to analyze a description of the machine, and based on the description, to determine from the description whether cycles output by the resource require translation from one bus to another bus, and if so, to dynamically provide a translator to change a cycle type for the resource based on translation that will be performed at the bus bridge; and

a second component configured to obtain the translator from the first component, and further configured to tell the resource to output translated cycles based on information in the translator.

16. (Previously presented) The system of claim 15 wherein the bus bridge comprises a CPU to PCI bridge.

17. (Previously presented) The system of claim 15 wherein the bus bridge comprises a PCI to ISA bridge.

18. (Previously presented) The system of claim 15 wherein the first component comprises an ACPI driver.

19. (Previously presented) The system of claim 15 wherein the other component comprises an operating system component.

20. (Previously presented) The system of claim 19 wherein the other component comprises a Plug and Play component.

21. (Previously presented) The system of claim 15 wherein the description of the machine is provided in firmware information.

22. (Previously presented) The system of claim 21 wherein the first component constructs a namespace from the firmware information.

23. (Previously presented) The system of claim 15 wherein the first component performs a translation.

24. (Canceled)

25. (Canceled)

26. (Previously presented) The system of claim 15 wherein the cycle type comprises I/O and is changed to memory.

27. (Previously presented) The system of claim 15 wherein the cycle type comprises memory and is changed to I/O.

28. (Canceled)

29 - 32. (Canceled)

33. (Previously presented) A system for configuring a resource to communicate with a device, comprising:

a bus bridge to which the device is connected;

a first component configured to analyze a description of the machine, and based on the description, to provide a translator for the resource based on translation that will be performed at the bus bridge, the first component providing the translator to change a memory address; and

a second component configured to obtain the translator from the first component, and further configured to tell the resource to output cycles based on information in to the translator.

34. (Previously presented) The system of claim 33 wherein the bus bridge comprises a CPU to PCI bridge.

35. (Previously presented) The system of claim 33 wherein the bus bridge comprises a PCI to ISA bridge.

36. (Previously presented) The system of claim 33 wherein the first component comprises an ACPI driver.

37. (Previously presented) The system of claim 33 wherein the other component comprises an operating system component.

38. (Previously presented) The system of claim 33 wherein the description of the machine is provided in firmware information, and wherein the first component constructs a namespace from the firmware information.

39. (Previously presented) The system of claim 33 wherein the first component performs a translation.

40. (Previously presented) A system for configuring a resource to communicate with a device, comprising:

a bus bridge to which the device is connected;

a first component configured to analyze a description of the machine, and based on the description, to provide a translator for the resource based on translation that will be performed at the bus bridge, the first component providing the translator to change a cycle type; and

a second component configured to obtain the translator from the first component, and further configured to tell the resource to output cycles based on information in to the translator.

41. (Previously presented) The system of claim 40 wherein the cycle type comprises I/O and is changed to memory.

42. (Previously presented) The system of claim 40 wherein the cycle type comprises memory and is changed to I/O.

43. (Previously presented) The system of claim 40 wherein the bus bridge comprises a CPU to PCI bridge.

44. (Previously presented) The system of claim 40 wherein the bus bridge comprises a PCI to ISA bridge.

45. (Previously presented) The system of claim 40 wherein the first component comprises an ACPI driver.

46. (Previously presented) The system of claim 40 wherein the other component comprises an operating system component.

47. (Previously presented) The system of claim 40 wherein the description of the machine is provided in firmware information, and wherein the first component constructs a namespace from the firmware information.

48. (Previously presented) The system of claim 40 wherein the first component performs a translation.

49. (Previously presented) A system for configuring a resource to communicate with a device, comprising:

a bus bridge to which the device is connected,

a first component configured to analyze a description of the machine, and based on the description, to determine from the description whether cycles output by the resource require translation from one bus to another bus, and if so, to dynamically provide a translator to change a memory address for the resource based on translation that will be performed at the bus bridge; and

a second component configured to obtain the translator from the first component, and further configured to tell the resource to output translated cycles based on information in the translator.